

## PHASE CONTROL THYRISTORS

- **Junction Size:** Square 590 mils
- **Wafer Size:** 4"
- **V<sub>RRM</sub> Class:** 600 to 1200 V
- **Passivation Process:** Glassivated MESA
- **Reference IR Packaged Part:** 181RKI Series

### Major Ratings and Characteristics

Parameters	Units	Test Conditions
V <sub>TM</sub> Maximum On-state Voltage	1.2 V	T <sub>J</sub> = 25°C, I <sub>T</sub> = 25 A
V <sub>DRM</sub> /V <sub>RRM</sub> Direct and Reverse Breakdown Voltage	600 to 1200 V	T <sub>J</sub> = 25°C, I <sub>DRM</sub> /I <sub>RRM</sub> = 100 μA (1)
I <sub>GT</sub> Max. Required DC Gate Current to Trigger	140 mA	T <sub>J</sub> = 25°C, anode supply = 6 V, resistive load
V <sub>GT</sub> Max. Required DC Gate Voltage to Trigger	2 V	T <sub>J</sub> = 25°C, anode supply = 6 V, resistive load
I <sub>H</sub> Holding Current Range	5 to 180 mA	Anode supply = 6 V, resistive load
I <sub>L</sub> Maximum Latching Current	900 mA	Anode supply = 6 V, resistive load

(1) Nitrogen flow on die edge.

### Mechanical Characteristics

Nominal Back Metal Composition, Thickness	Cr - Ni - Ag (1 KA - 4 KA - 6 KA)
Nominal Front Metal Composition, Thickness	Cr - Ni - Ag (1 KA - 4 KA - 6 KA)
Chip Dimensions	590 x 590 mils (see drawing)
Wafer Diameter	100 mm, with std. <110> flat
Wafer Thickness	370 μm ± 10 μm
Maximum Width of Sawing Line	130 μm
Reject Ink Dot Size	0.25 mm diameter minimum
Ink Dot Location	See drawing
Recommended Storage Environment	Storage in original container, in dessicated nitrogen, with no contamination

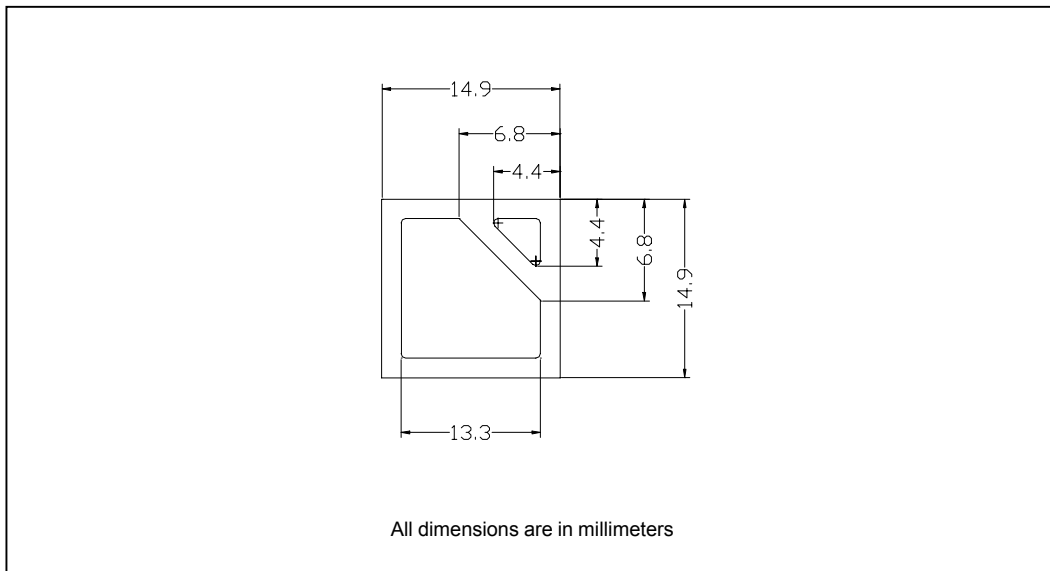
Ordering Information Table

Device Code						
IR	590	S	G	12	H	CB
①	②	③	④	⑤	⑥	⑦

<p><b>1</b> - International Rectifier Device</p> <p><b>2</b> - Chip Dimension in Mils</p> <p><b>3</b> - Type of Device: S = Solderable SCR</p> <p><b>4</b> - Passivation Process: G = Glassivated MESA</p> <p><b>5</b> - Voltage code: Code x 100 = <math>V_{RRM}</math></p> <p><b>6</b> - Metallization: H = Silver (Anode) - Silver (Cathode)</p> <p><b>7</b> - CB = Probed Uncut Die (wafer in box)                  None = Probed Die in chip carrier</p>	<table border="1"> <thead> <tr> <th>Available Class</th> </tr> </thead> <tbody> <tr> <td>06 = 600 V</td> </tr> <tr> <td>08 = 800 V</td> </tr> <tr> <td>12 = 1200 V</td> </tr> </tbody> </table>	Available Class	06 = 600 V	08 = 800 V	12 = 1200 V
Available Class					
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Outline Table



### Wafer Layout

